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Circuit

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3-The FET: Design

Example 3.5

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Law (KCL) - How to

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- Lecture 25 (Fall
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Assuming that the
diodes in the
circuits of Fig. P4.9
are ideal, find the
values of the
labeled

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Example 3.6 How
To Solve Diode
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Using Ohm's Law
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Effect Transistors
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$$\begin{aligned} \text{EX1.8 } I_D &= I_S \exp\left(\frac{V_D}{V_T}\right) \\ &= 4 \times 10^{-12} \exp\left(\frac{V_D}{0.025}\right) \\ &= 4 \times 10^{-12} \exp\left(\frac{4 - V_D}{0.025}\right) \end{aligned}$$

so $4 = I_D (4 \times 10^3) + V_D$
 $I_D = 4 \times 10^3$ and
 $V_D = 10 - 12$

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) $\exp \left[\frac{V_D}{V_T} \right] = 0.026$

By trial and error, we find $I_D = 0.866$

mA and $V_D = 0.535$ V. _____

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